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FIG. 2 (Amended)

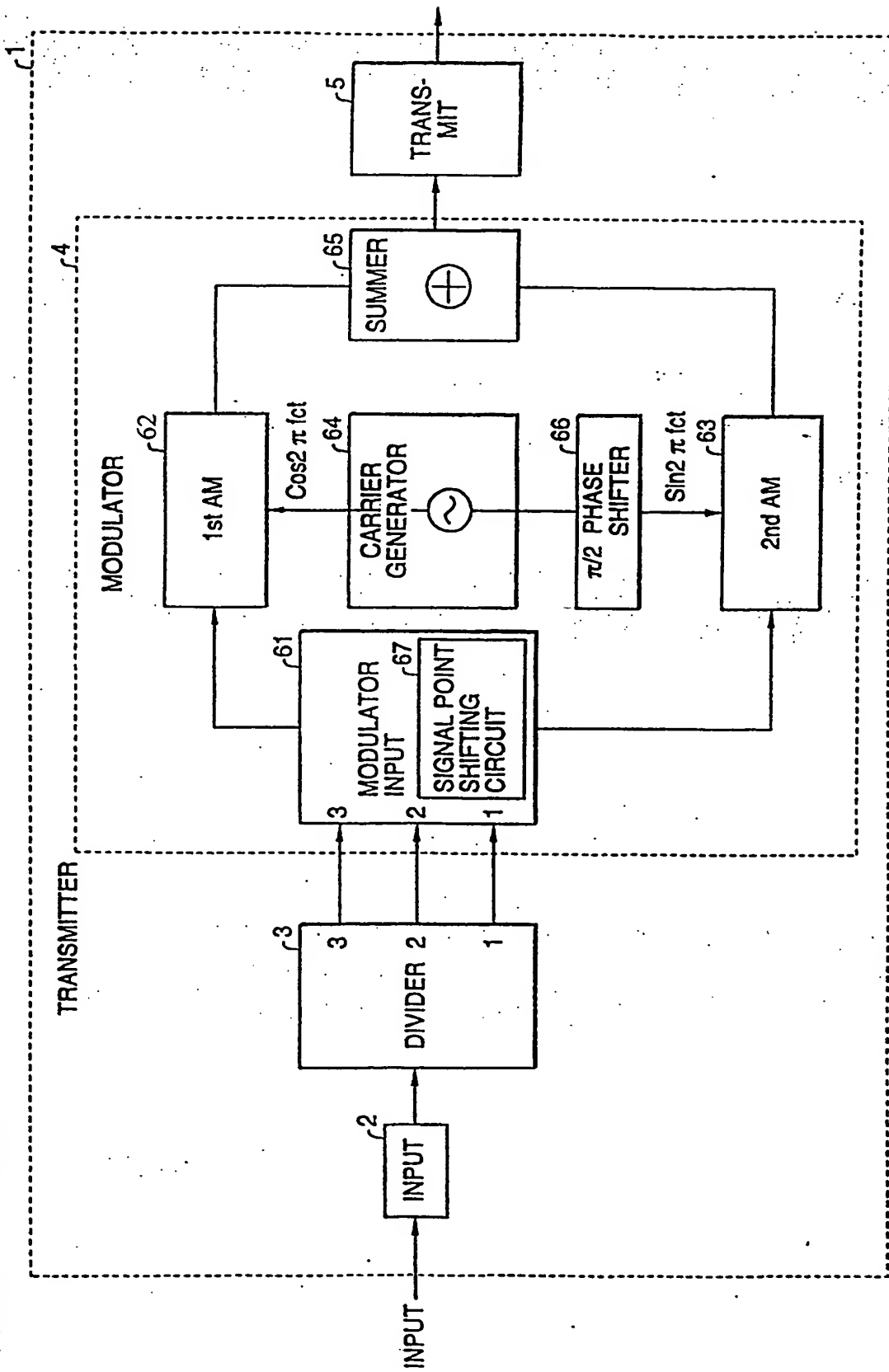


FIG. 10 (Amended)

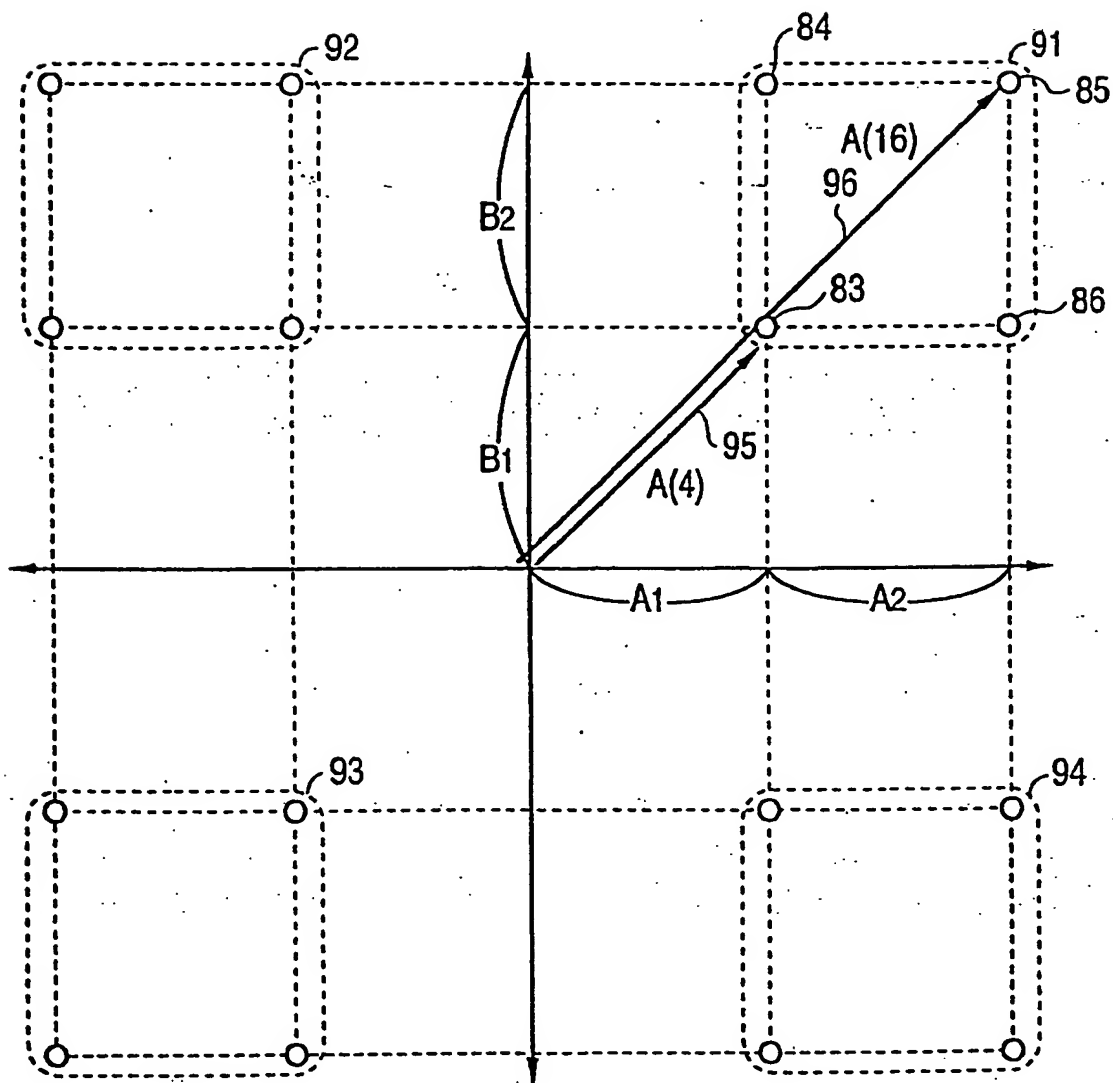


FIG. 17 (Amended)

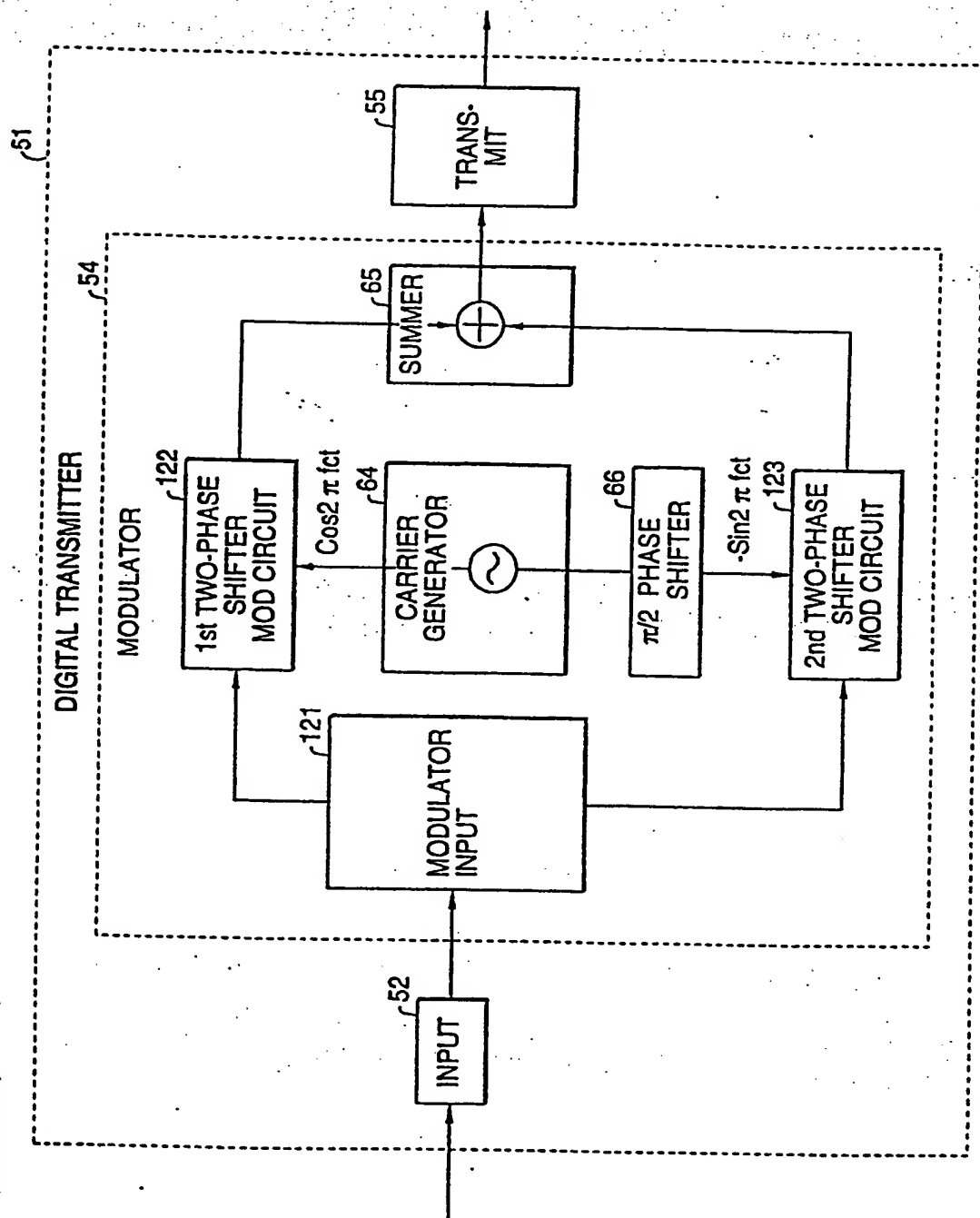


FIG. 29 (Amended)

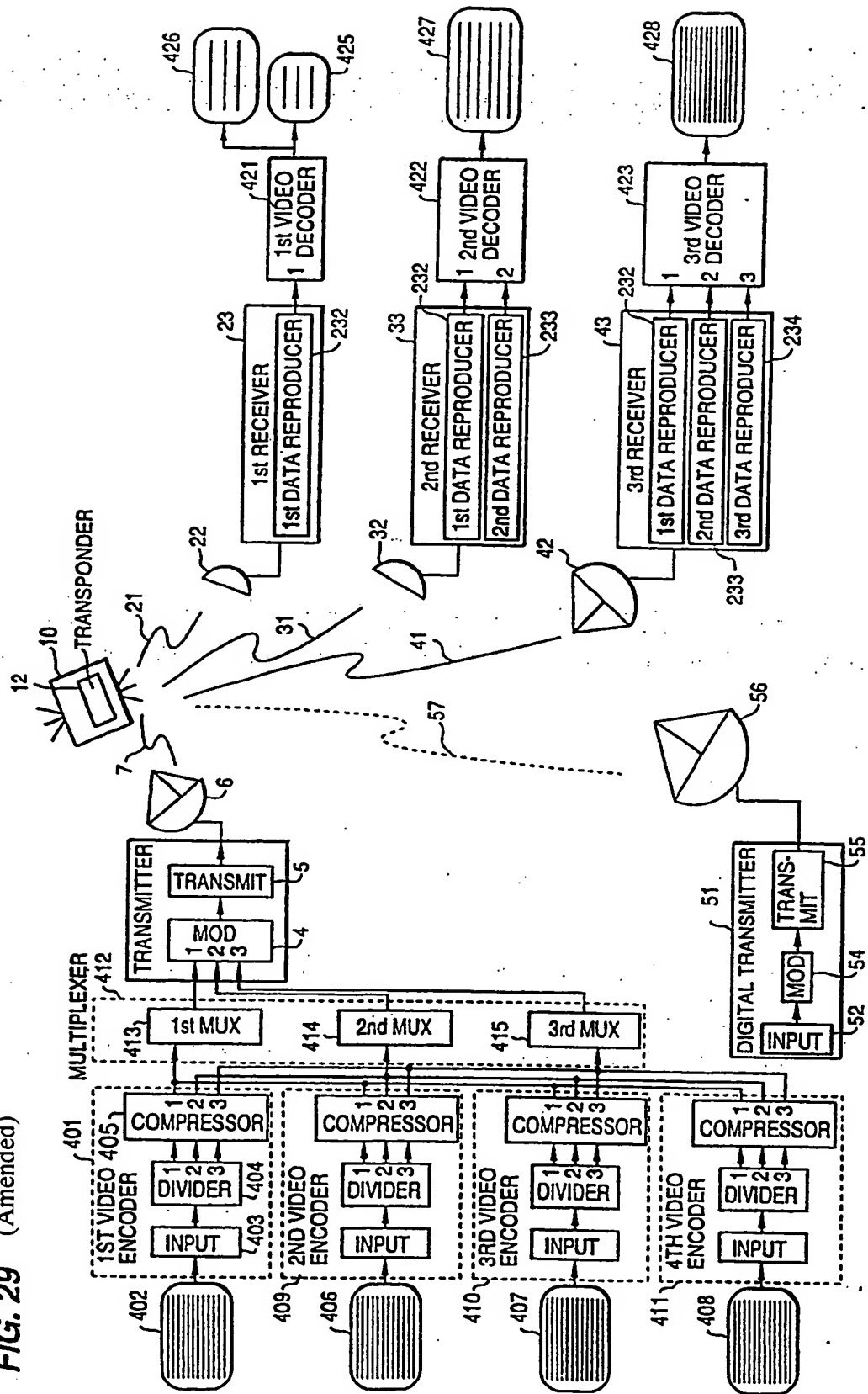


FIG. 48 (Amended)

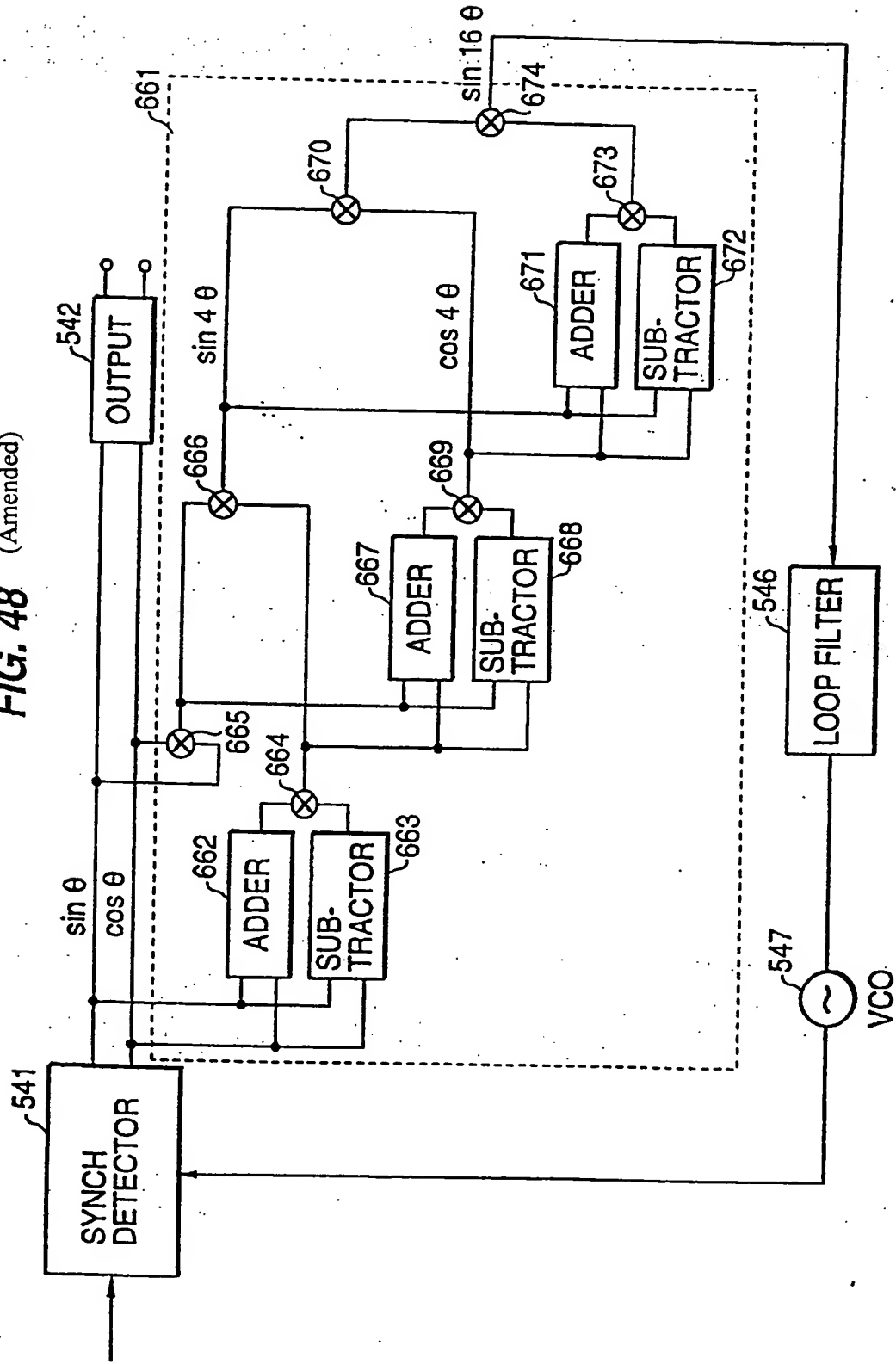
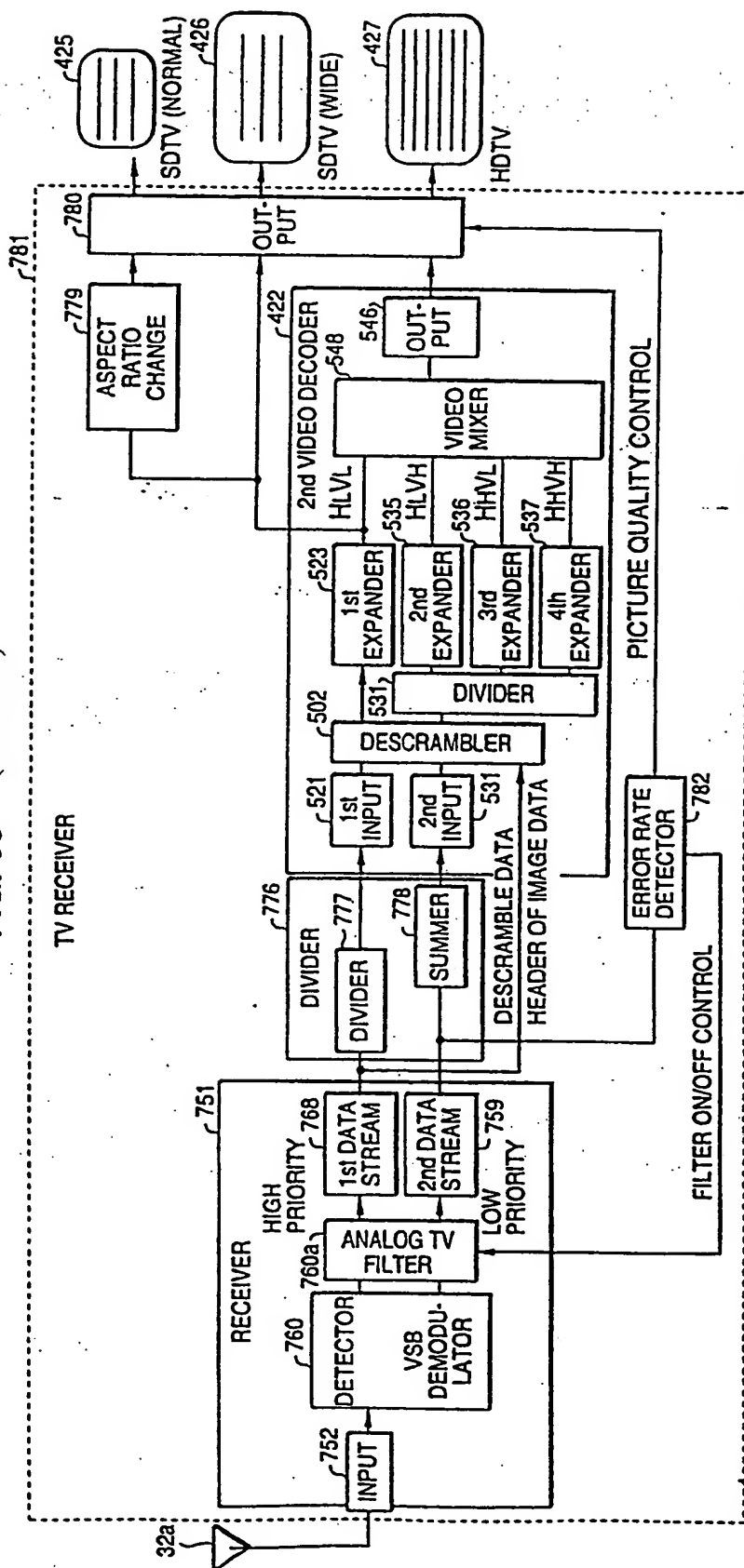


FIG. 65



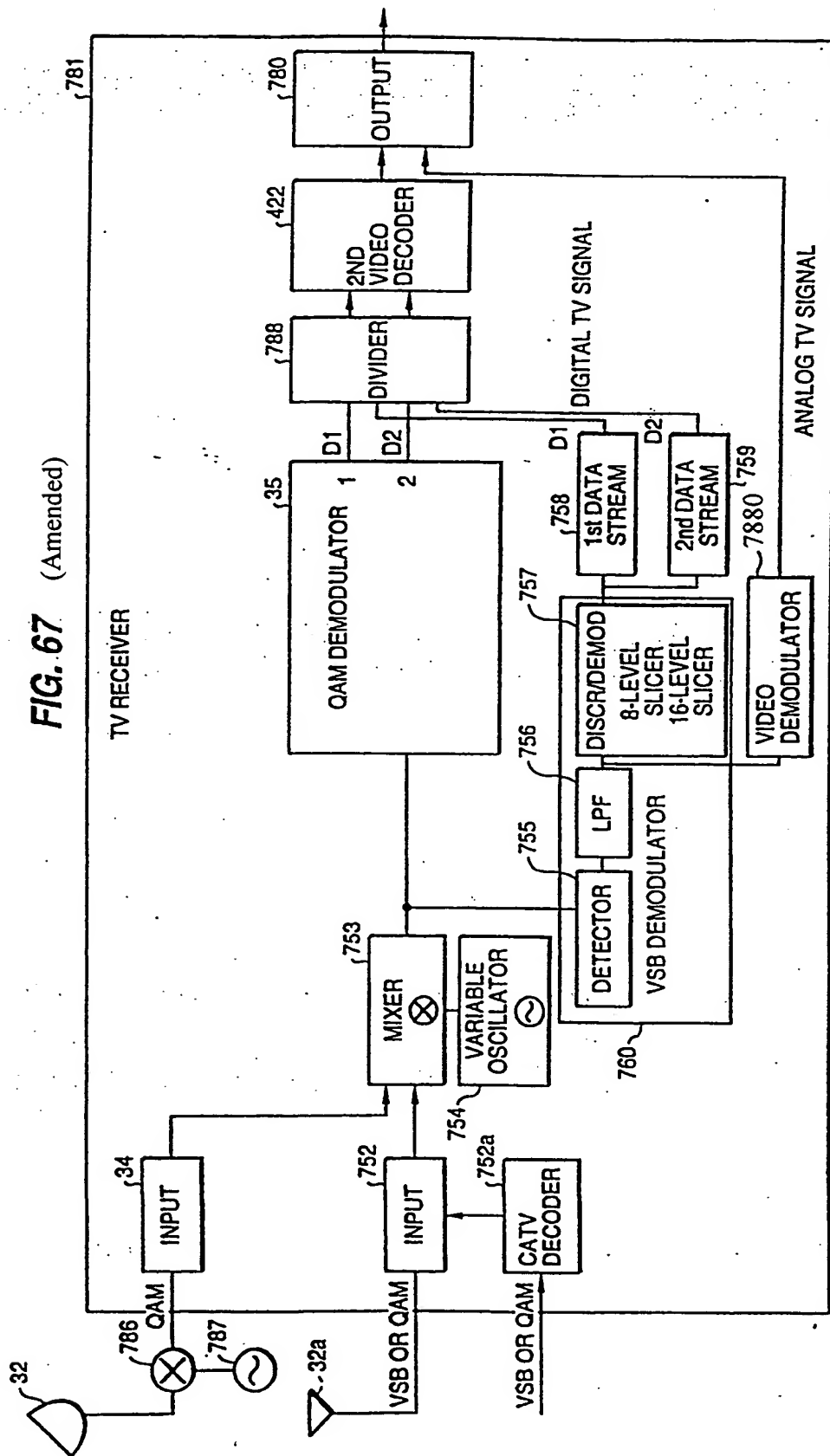


FIG. 67 (Amended)

FIG. 93 (Amended)

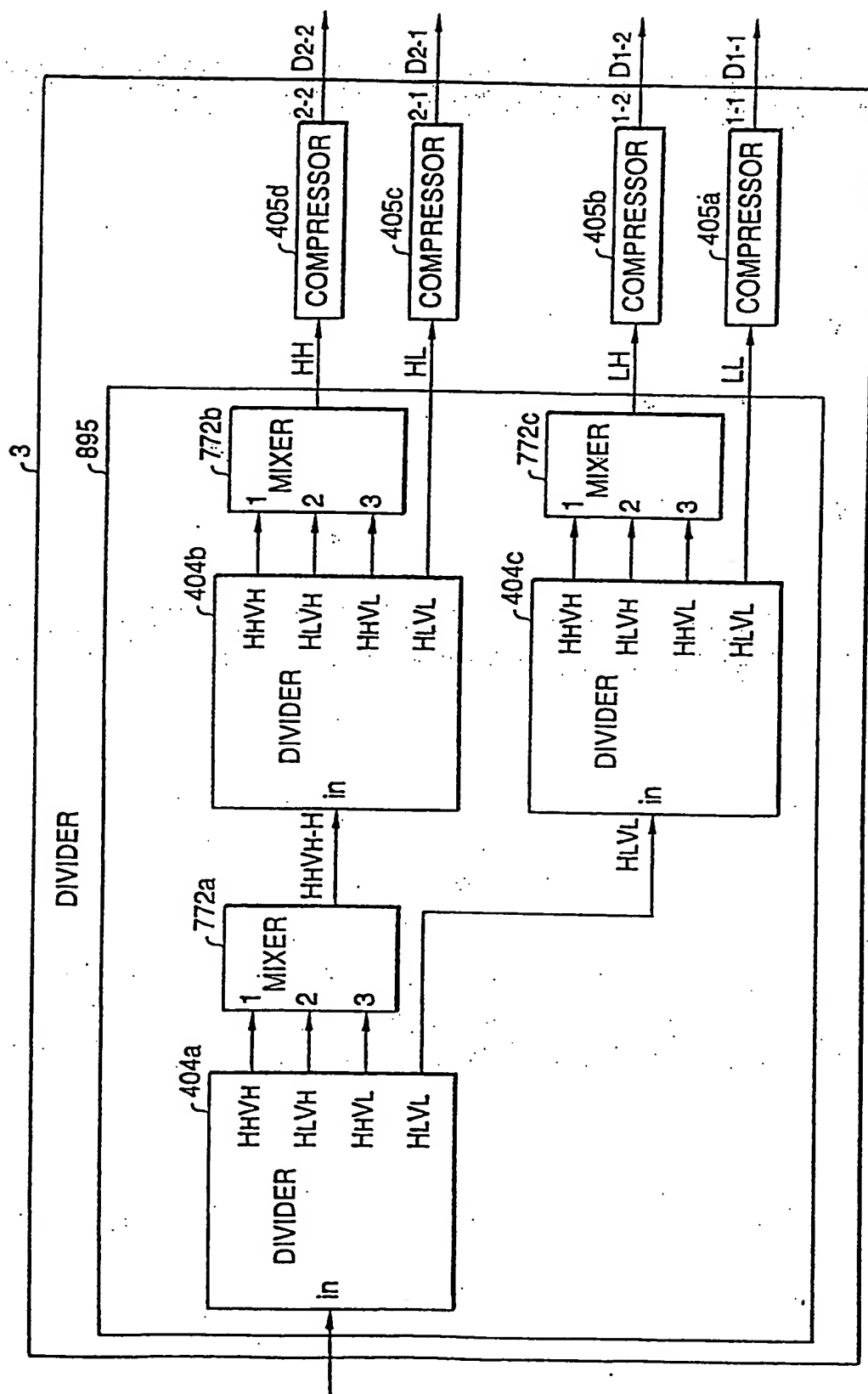
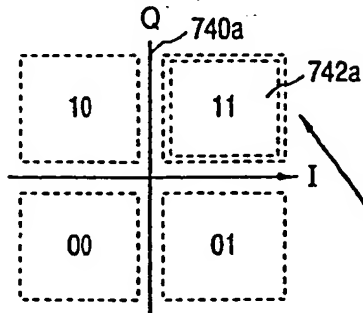
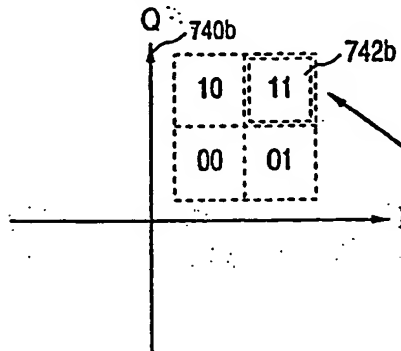


FIG. 112 (Amended)

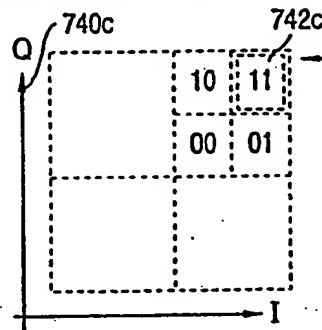
SUBCHANNEL-1 (SRQAM:D1 = 2bit)



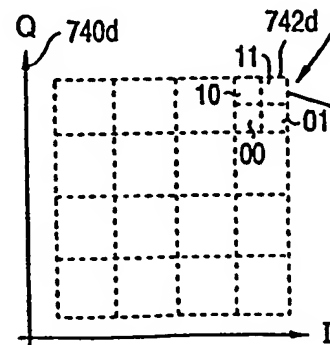
SUBCHANNEL-2 (16-SRQAM:D2 = 2bit)



SUBCHANNEL-3 (64-SRQAM:D3 = 2bit)



SUBCHANNEL-4 (256-SRQAM:D4 = 2bit)



CODE WORD-1

CODE WORD-2

CODE WORD-3

CODE WORD-4

741a 11 2bit

741b 11 2bit

741c 11 2bit

741d 11 2bit

8bit

SIGNAL POINT
CODE WORD
1111 1111

741

FIG. 131 (Amended)

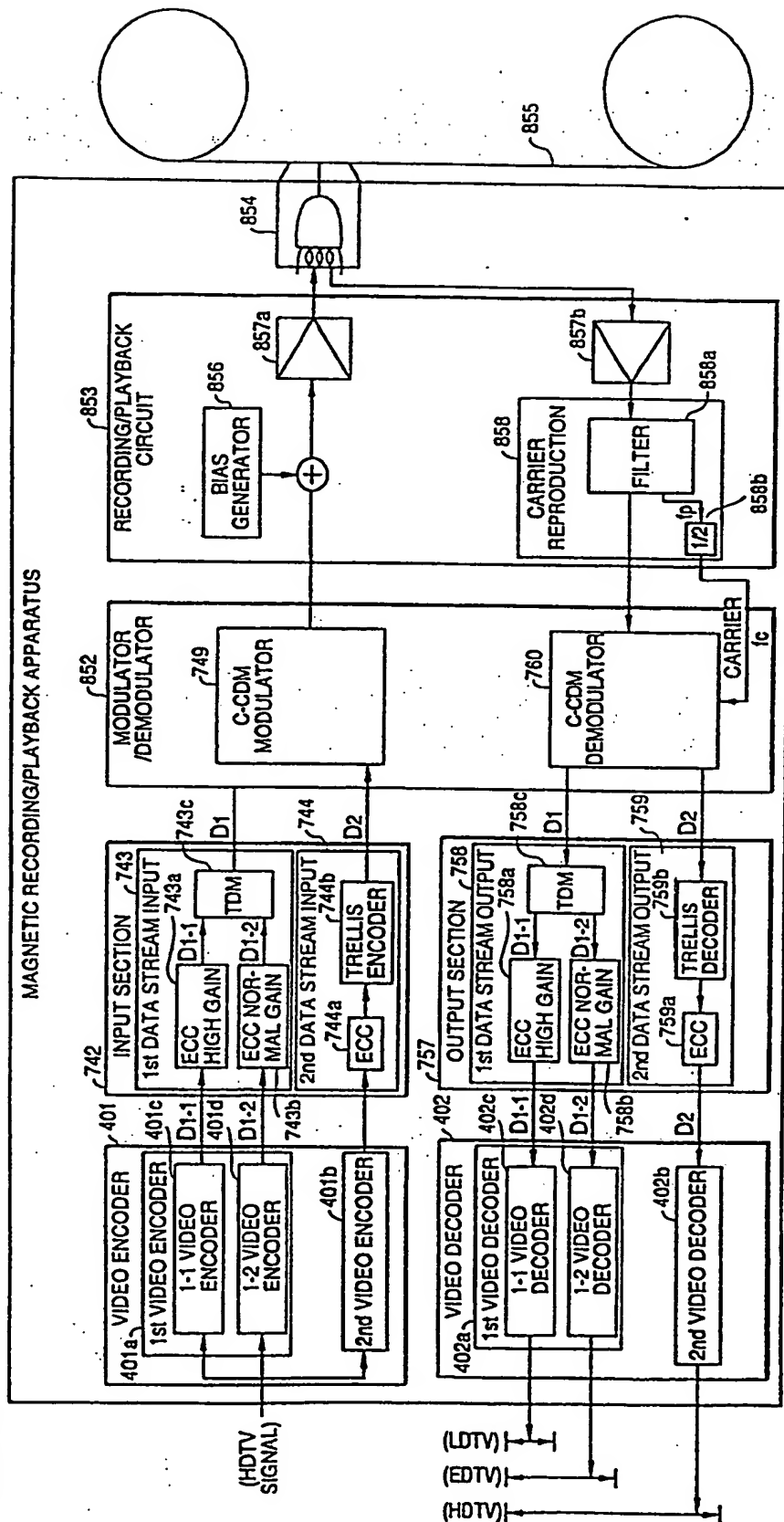


FIG. 138 (Amended)

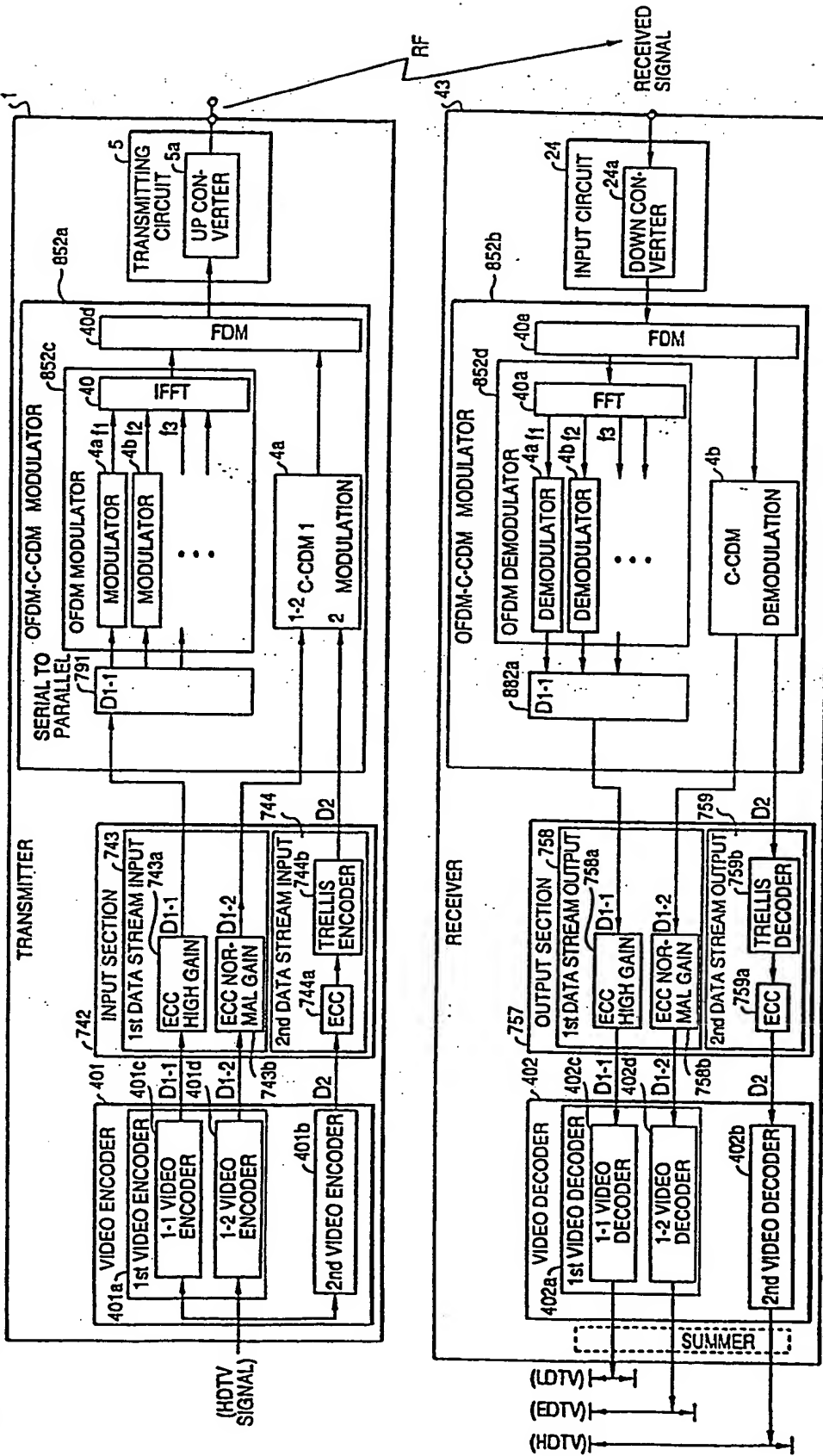


FIG. 144 (Amended)

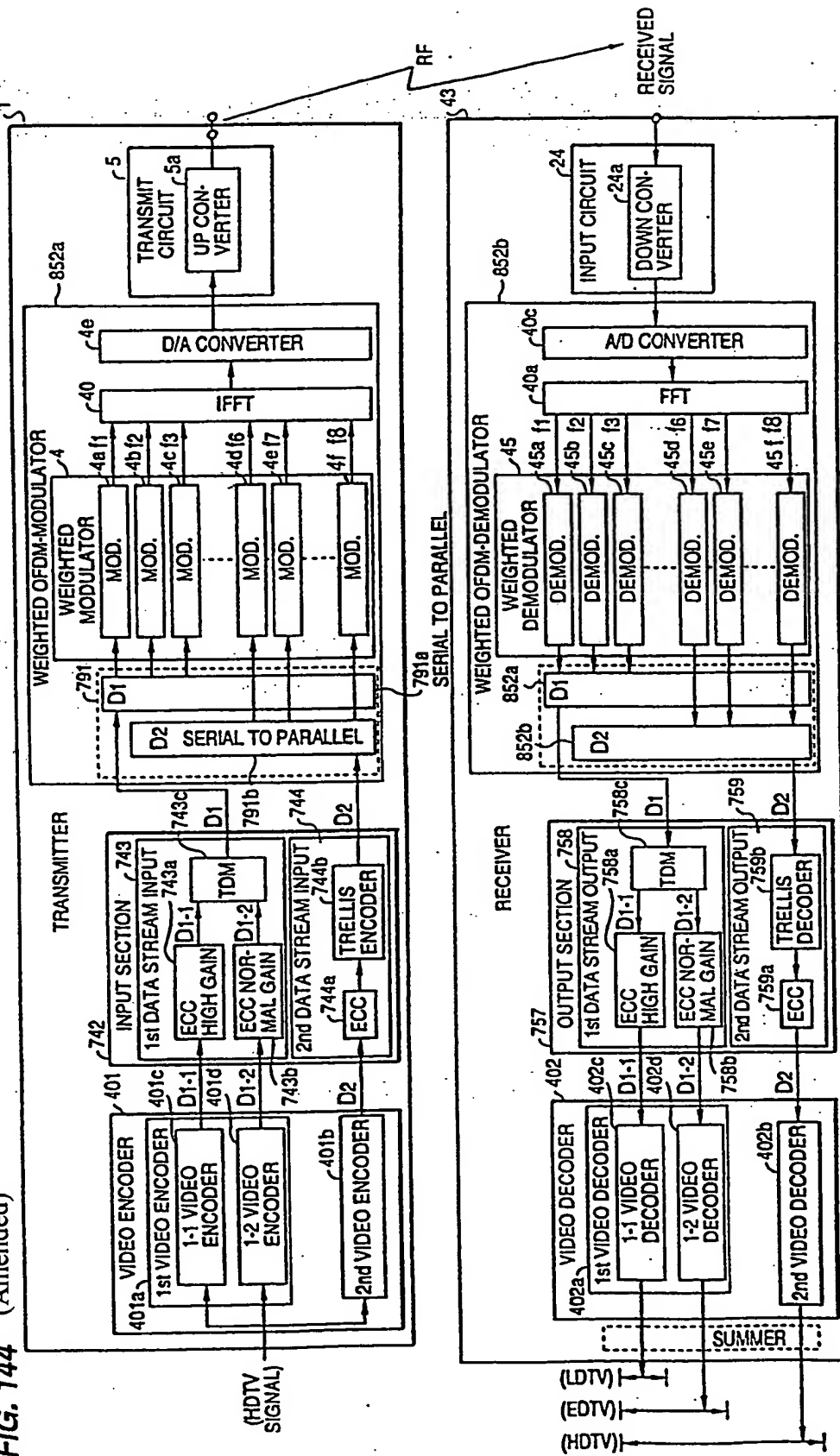


FIG. 169 (Amended)

